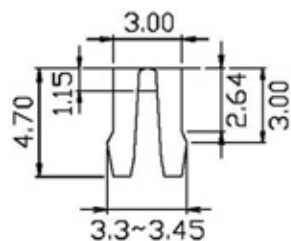
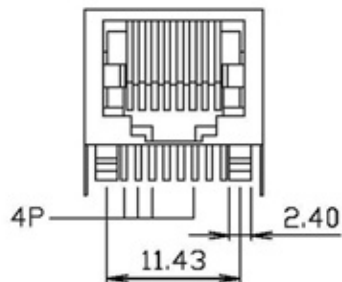


NOTE: A--INSULATOR(TAPE)  
B--MUST COVER OVER ALL CONTACTS

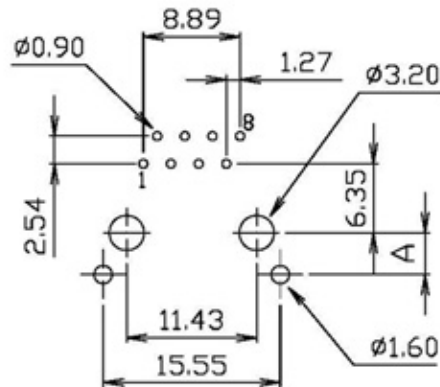
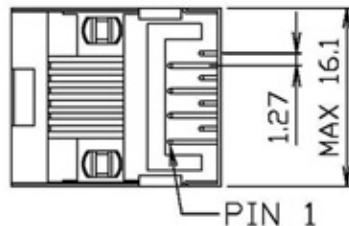
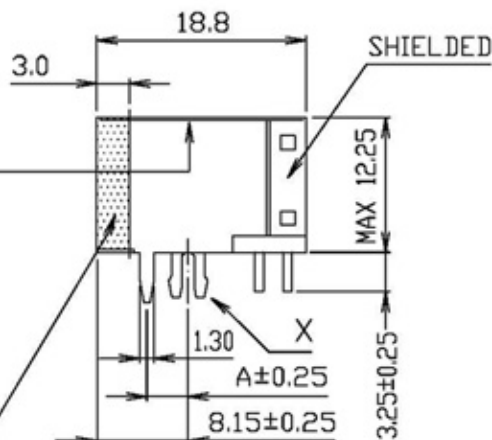


DETAIL X

Define The Area

WIDTH DIM. =  $15.70^{+0.15}_{-0.10}$

HIGH DIM. =  $12.00^{+0.15}_{-0.10}$



PC Board Layout

|                         |                                        |                                                   |                                                       |                   |                    |
|-------------------------|----------------------------------------|---------------------------------------------------|-------------------------------------------------------|-------------------|--------------------|
| DO NOT SCALE<br>DRAWING | TOLERANCES UNLESS<br>OTHERWISE NOTED   | INCH                                              | MM                                                    | DR.<br>H.W.CHAN   | DATE<br>OCT-11 '07 |
| THIRD ANGLE PROJECTION  | LINEAR $\frac{\text{MM}}{\text{INCH}}$ | $00 \pm .01$<br>$000 \pm .005$<br>$0000 \pm .002$ | $0.0 \pm 0.25$<br>$0.00 \pm 0.13$<br>$0.000 \pm 0.05$ | CK'D<br>M.CHANG   | OCT-11 '07         |
| MM<br>INCH              | ANGLE $\pm 0^\circ 30'$                | RADII                                             | $\pm 0.40$<br>$- 0.15$                                | APP'D<br>W.J.YANG | OCT-12 '07         |

E-YUNG ENTERPRISE CO., LTD.

TITLE 8POSITION SIDE ENTRY  
VERY LOW PROFILE, SHIELDED PCB JACK

SCALE SIZE DWG.NO.  
969405

REV. SHEET  
2 1